

Day : Thursday

Date: 11/3/2005

Time: 14:19:38

PALM INTRANET

**Inventor Name Search Result**

Your Search was:

Last Name = RASHID

First Name = ABBAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09377503</u>	<u>6717576</u>	150	08/20/1999	DEFERRED SHADINGGRAPHICS PIPELINE PROCESSOR HAVING ADVANCED FEATURES	RASHID, ABBAS
<u>09378408</u>	<u>6288730</u>	150	08/20/1999	METHOD AND APPARATUS FOR GENERATING TEXTURE	RASHID, ABBAS
<u>09724663</u>	Not Issued	71	11/28/2000	Method and apparatus for generating texture	RASHID, ABBAS
<u>09900514</u>	Not Issued	80	07/06/2001	Cross-bar switch	RASHID, ABBAS
<u>10036595</u>	Not Issued	94	12/21/2001	CROSS-BAR SWITCH WITH BANDWIDTH ALLOCATION	RASHID, ABBAS
<u>10036602</u>	Not Issued	71	12/21/2001	Cross-bar switch with explicit multicast support	RASHID, ABBAS
<u>10036603</u>	Not Issued	71	12/21/2001	Cross-bar switch supporting implicit multicast addressing	RASHID, ABBAS
<u>10036622</u>	Not Issued	41	12/21/2001	Cross-bar switch employing a multiple entry point FIFO	RASHID, ABBAS
<u>10036762</u>	Not Issued	71	12/21/2001	Cross-bar switch incorporating a sink port with retry capability	RASHID, ABBAS
<u>10037144</u>	Not Issued	41	12/21/2001	Cross-bar switch with sink port accepting multiple packets	RASHID, ABBAS
<u>10191742</u>	Not Issued	41	07/08/2002	Content service aggregation system	RASHID, ABBAS
<u>10458493</u>	Not Issued	41	06/09/2003	Deferred shading graphics pipeline processor having advanced features	RASHID, ABBAS
<u>10682579</u>	Not Issued	41	10/08/2003	Advanced telecommunications processor	RASHID, ABBAS
<u>10898007</u>	Not	30	07/23/2004	Advanced processor scheduling	RASHID, ABBAS

	Issued			in a multithreaded system	
<u>10898008</u>	Not Issued	41	07/23/2004	Advanced processor with system on a chip interconnect technology	RASHID, ABBAS
<u>10930175</u>	Not Issued	20	08/31/2004	Advanced processor with a thread aware return address stack optimally used across active threads	RASHID, ABBAS
<u>10930186</u>	Not Issued	20	08/31/2004	Advanced processor with scheme for optimal packet flow in a multi-processor system on a chip	RASHID, ABBAS
<u>10930455</u>	Not Issued	30	08/31/2004	Advanced processor with mechanism for fast packet queuing operations	RASHID, ABBAS
<u>10930937</u>	Not Issued	30	08/31/2004	Advanced processor with interfacing messaging network to a CPU	RASHID, ABBAS
<u>10930939</u>	Not Issued	30	08/31/2004	Advanced processor with mechanism for maximizing resource usage in an in-order pipeline with multiple threads	RASHID, ABBAS
<u>10931003</u>	Not Issued	20	08/31/2004	Advanced processor with interrupt delivery mechanism for multi-threaded multi-CPU system on a chip	RASHID, ABBAS
<u>60303354</u>	Not Issued	159	07/06/2001	Content service aggregation system	RASHID, ABBAS
<u>07835030</u>	Not Issued	161	02/10/1992	METHOD FOR INTERFERON PRODUCTION FROM HUMAN PERIPHERAL BLOOD LEUKOCYTES AND ALPHA INTERFERON PRODUCED THEREBY	RASHIDBAIGI, ABBAS
<u>08129089</u>	<u>5503828</u>	150	10/05/1993	ALPHA INTERFERON COMPOSITION AND METHOD FOR ITS PRODUCTION FROM HUMAN PERIPHERAL BLOOD LEUKOCYTES	RASHIDBAIGI, ABBAS
<u>08144601</u>	Not Issued	164	10/27/1993	COMPOSITION CONTAINING HUMAN ALPHA INTERFERON SPECIES PROTEINS AND METHOD FOR USE THEREOF	RASHIDBAIGI, ABBAS
<u>08454444</u>	<u>5676942</u>	150	05/25/1995	COMPOSITION CONTAINING	RASHIDBAIGI,

				HUMAN ALPHA INTERFERON SPECIES PROTEINS AND METHOD FOR USE THEREOF	ABBAS
<u>08996070</u>	<u>5989441</u>	150	12/22/1997	RECOVERY OF FUNCTIONAL HUMAN LEUKOCYTES FROM RECYCLED FILTERS	RASHIDBAIGI, ABBAS

Inventor Search Completed: No Records to Display.

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	<b>Last Name</b>	<b>First Name</b>	
<b>Search Another: Inventor</b>	<input type="text" value="RASHID"/>	<input type="text" value="ABBAS"/>	<input type="button" value="Search"/>

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Day : Thursday  
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# PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = ZAIDI

First Name = NAZAR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08780249</a>	<a href="#">6016540</a>	150	01/08/1997	METHOD AND APPARATUS FOR SCHEDULING INSTRUCTIONS IN WAVES	ZAIDI, NAZAR
<a href="#">08780255</a>	<a href="#">6065105</a>	150	01/08/1997	DEPENDENCY MATRIX	ZAIDI, NAZAR
<a href="#">08986008</a>	<a href="#">6032250</a>	150	12/05/1997	METHOD AND APPARATUS FOR IDENTIFYING INSTRUCTION BOUNDARIES	ZAIDI, NAZAR
<a href="#">09209094</a>	<a href="#">6292882</a>	150	12/10/1998	METHOD AND APPARATUS FOR FILTERING VALID INFORMATION FOR DOWNSTREAM PROCESSING	ZAIDI, NAZAR
<a href="#">09900481</a>	<a href="#">6839808</a>	150	07/06/2001	"PROCESSING CLUSTER HAVING MULTIPLE COMPUTE ENGINES AND SHARES TIER ONE CACHES"	ZAIDI, NAZAR
<a href="#">09900514</a>	Not Issued	80	07/06/2001	Cross-bar switch	ZAIDI, NAZAR
<a href="#">10036595</a>	Not Issued	94	12/21/2001	CROSS-BAR SWITCH WITH BANDWIDTH ALLOCATION	ZAIDI, NAZAR
<a href="#">10036602</a>	Not Issued	71	12/21/2001	Cross-bar switch with explicit multicast support	ZAIDI, NAZAR
<a href="#">10036603</a>	Not Issued	71	12/21/2001	Cross-bar switch supporting implicit multicast addressing	ZAIDI, NAZAR
<a href="#">10036622</a>	Not Issued	41	12/21/2001	Cross-bar switch employing a multiple entry point FIFO	ZAIDI, NAZAR
<a href="#">10036762</a>	Not Issued	71	12/21/2001	Cross-bar switch incorporating a sink port with retry capability	ZAIDI, NAZAR
<a href="#">10037144</a>	Not Issued	41	12/21/2001	Cross-bar switch with sink port accepting multiple packets	ZAIDI, NAZAR
<a href="#">10105151</a>	<a href="#">6745289</a>	150	03/25/2002	PROCESSING PACKETS IN CACHE MEMORY	ZAIDI, NAZAR

<u>10105924</u>	<u>6880049</u>	150	03/25/2002	SHARING A SECOND TIER CACHE MEMORY IN A MULTI- PROCESSOR	ZAIDI, NAZAR
<u>10105972</u>	<u>6895477</u>	150	03/25/2002	RING-BASED MEMORY REQUESTS IN A SHARED MEMORY MULTI-PROCESSOR	ZAIDI, NAZAR
<u>10105973</u>	<u>6898673</u>	150	03/25/2002	CO-PROCESSOR INCLUDING A MEDIA ACCESS CONTROLLER	ZAIDI, NAZAR
<u>10105979</u>	<u>6920542</u>	150	03/25/2002	APPLICATION PROCESSING EMPLOYING A COPROCESSOR	ZAIDI, NAZAR
<u>10105993</u>	<u>6892282</u>	150	03/25/2002	RING BASED MULTI- PROCESSING SYSTEM	ZAIDI, NAZAR
<u>10106925</u>	<u>6901482</u>	150	03/25/2002	MANAGING OWNERSHIP OF A FULL CACHE LINE USING A STORE-CREATE OPERATION	ZAIDI, NAZAR
<u>10191742</u>	Not Issued	41	07/08/2002	Content service aggregation system	ZAIDI, NAZAR
<u>10191746</u>	Not Issued	30	07/08/2002	Content service aggregation system control architecture	ZAIDI, NAZAR
<u>60120396</u>	Not Issued	159	02/17/1999	METHOD OF DYNAMICALLY EXPANDING AND EXECUTING MICROCODE ROUTINES	ZAIDI, NAZAR
<u>60303354</u>	Not Issued	159	07/06/2001	Content service aggregation system	ZAIDI, NAZAR
<u>09001251</u>	<u>5996064</u>	150	12/30/1997	METHOD AND APPARATUS FOR GUARANTEEING MINIMUM VARIABLE SCHEDULE DISTANCE BY USING POST-READY LATENCY	ZAIDI, NAZAR A.
<u>09520424</u>	<u>6574689</u>	150	03/08/2000	METHOD AND APPARATUS FOR LIVE-LOCK PREVENTION	ZAIDI, NAZAR A.
<u>10247894</u>	Not Issued	41	09/19/2002	Processor utilizing novel architectural ordering scheme	ZAIDI, NAZAR A.
<u>10682579</u>	Not Issued	41	10/08/2003	Advanced telecommunications processor	ZAIDI, NAZAR A.
<u>60416838</u>	Not Issued	159	10/08/2002	Advanced telecommunications processor	ZAIDI, NAZAR A.
<u>60490236</u>	Not Issued	159	07/25/2003	Advanced telecommunications processor	ZAIDI, NAZAR A.
<u>08672409</u>	<u>5944818</u>	150	06/28/1996	METHOD & APPARATUS FOR ACCELERATED INSTRUCTION RESTART IN A MICROPROCESSOR	ZAIDI, NAZAR A.

<u>08769068</u>	<u>5918031</u>	150	12/18/1996	COMPUTER UTILIZING SPECIAL MICRO-OPERATIONS FOR ENCODING OF MULTIPLE VARIANT CODE FLOWS	ZAIDI, NAZAR A.
<u>08779791</u>	<u>6049897</u>	150	01/07/1997	MULTIPLE SEGMENT REGISTER USE WITH DIFFERENT OPERAND SIZE	ZAIDI, NAZAR A.
<u>08994400</u>	<u>5954814</u>	150	12/19/1997	SYSTEM FOR USING A BRANCH PREDICTION UNIT TO ACHIEVE SERIALIZATION BY FORCING A BRANCH MISPREDICTION TO FLUSH A PIPELINE	ZAIDI, NAZAR A.
<u>09001170</u>	<u>5961630</u>	150	12/30/1997	METHOD AND APPARATUS FOR HANDLING DYNAMIC STRUCTURAL HAZARDS AND EXCEPTIONS BY USING POST-READY LATENCY	ZAIDI, NAZAR A.
<u>09001741</u>	<u>6216221</u>	150	12/31/1997	METHOD AND APPARATUS FOR EXPANDING INSTRUCTIONS	ZAIDI, NAZAR A.
<u>09002236</u>	<u>5961615</u>	150	12/31/1997	METHOD AND APPARATUS FOR QUEUING DATA	ZAIDI, NAZAR A.
<u>09002774</u>	<u>6044456</u>	150	01/05/1998	SYSTEM AND METHOD TO MAINTAIN SYNCHRONIZATION OF DATA BETWEEN MULTIPLE PARALLEL FRONT-END PIPELINES	ZAIDI, NAZAR A.
<u>09088634</u>	<u>6405307</u>	150	06/02/1998	APPARATUS AND METHOD FOR DETECTING AND HANDLING SELF-MODIFYING CODE CONFLICTS IN AN INSTRUCTION FETCH PIPELINE	ZAIDI, NAZAR A.
<u>09089736</u>	<u>6237088</u>	150	06/03/1998	SYSTEM AND METHOD FOR TRACKING IN-FLIGHT INSTRUCTIONS IN A PIPELINE	ZAIDI, NAZAR A.
<u>09183084</u>	<u>6363408</u>	150	10/30/1998	METHOD AND APPARATUS FOR SUMMING SELECTED BITS FROM A PLURALITY OF MACHINE VECTORS	ZAIDI, NAZAR A.
<u>09217774</u>	<u>6523106</u>	150	12/21/1998	METHOD AND APPARATUS FOR EFFICIENT PIPELINING	ZAIDI, NAZAR A.
<u>09314439</u>	<u>6055652</u>	150	05/19/1999	MULTIPLE SEGMENT REGISTER USE WITH	ZAIDI, NAZAR A.

				DIFFERENT OPERAND SIZE	
<a href="#">09476660</a>	<a href="#">6581154</a>	150	12/31/1999	EXPANDING MICROCODE ASSOCIATED WITH FULL AND PARTIAL WIDTH MACROINSTRUCTIONS	ZAIDI, NAZAR A.
<a href="#">09505949</a>	Not Issued	121	02/15/2000	Method and apparatus for achieving architectural correctness in a multi-mode processor providing floating-point support	ZAIDI, NAZAR ABBAS
<a href="#">09473577</a>	<a href="#">6542981</a>	150	12/28/1999	MICROCODE UPGRADE AND SPECIAL FUNCTION SUPPORT BY EXECUTING RISC INSTRUCTION TO INVOKE RESIDENT MICROCODE	ZAIDI, NAZAR ABBAS

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="ZAIDI"/>	<input type="text" value="NAZAR"/>	

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Day : Thursday  
Date: 11/3/2005


**PALM INTRANET**

Time: 14:20:02

**Inventor Name Search Result**

Your Search was:

Last Name = BRYERS

First Name = MARK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">07346648</a>	<a href="#">5058110</a>	150	05/03/1989	PROTOCOL PROCESSOR	BRYERS, MARK
<a href="#">09900514</a>	Not Issued	80	07/06/2001	Cross-bar switch	BRYERS, MARK
<a href="#">10036595</a>	Not Issued	94	12/21/2001	CROSS-BAR SWITCH WITH BANDWIDTH ALLOCATION	BRYERS, MARK
<a href="#">10036602</a>	Not Issued	71	12/21/2001	Cross-bar switch with explicit multicast support	BRYERS, MARK
<a href="#">10036603</a>	Not Issued	71	12/21/2001	Cross-bar switch supporting implicit multicast addressing	BRYERS, MARK
<a href="#">10036762</a>	Not Issued	71	12/21/2001	Cross-bar switch incorporating a sink port with retry capability	BRYERS, MARK
<a href="#">10037144</a>	Not Issued	41	12/21/2001	Cross-bar switch with sink port accepting multiple packets	BRYERS, MARK
<a href="#">10105508</a>	<a href="#">6938093</a>	150	03/25/2002	BANDWIDTH ALLOCATION FOR A DATA PATH	BRYERS, MARK
<a href="#">10191742</a>	Not Issued	41	07/08/2002	Content service aggregation system	BRYERS, MARK
<a href="#">10191746</a>	Not Issued	30	07/08/2002	Content service aggregation system control architecture	BRYERS, MARK
<a href="#">11189595</a>	Not Issued	30	07/26/2005	Network device having a co-processor including a media access controller and streaming engine	BRYERS, MARK
<a href="#">60027339</a>	Not Issued	159	10/01/1996	ROADRUNNER PCI/HIPPI CONTROLLER	BRYERS, MARK
<a href="#">60303354</a>	Not Issued	159	07/06/2001	Content service aggregation system	BRYERS, MARK

Inventor Search Completed: No Records to Display.

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	352	(370/424 370/412-418, 370/428-429)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 08:51
S2	6	S1 and ((crossbar cross-bar) same multicast\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 14:59
S3	11	S1 and ((crossbar cross-bar) same ring)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 13:44
S4	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 13:44
S5	1	S4 and plo	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 13:44
S6	4241	(370/424 370/412-418, 370/428-429).cls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/21 14:58
S7	49	S6 and ((crossbar cross-bar) same multicast\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:06
S8	352	(370/424 370/412-418, 370/428-429)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/21 14:59
S9	44	S7 not S8	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 14:59
S10	35	S6 and ((crossbar cross-bar) same ring)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:06
S11	11	S8 and ((crossbar cross-bar) same ring)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:07
S12	27	S10 not S11	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:11

S13	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 09:43
S14	1	S13 and ("370" with "390")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:41
S15	823	ack with ready	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:42
S16	102	ack with ready with (transmit transmission)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:43
S17	565	(transmission adj1 request) near3 packet	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 08:54
S18	45	((transmission adj1 request) near3 packet) same (confirm\$2 confirmation ack acknowledgement)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 09:28
S19	32657	(rts same cts)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 09:28
S20	2556	(rts same cts)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 09:29
S21	0	(rts same cts) and (request adj1 to adj1 send)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 09:29
S22	926	(rts same cts) and (request adj2 send)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 09:29
S23	894	(rts same cts) and (request adj2 send) and (clear adj2 send)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 09:43
S24	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:47

S25	1	S24 and (destination with address)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 09:45
S26	1	S24 and ("350" "351")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:40
S27	0	S24 and (crc error)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:20
S28	0	S24 and (crc same error same discard\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:21
S29	847	(crc same error same discard\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:21
S30	283	(crc with error with discard\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:21
S31	1	S24 and ("370" same "390")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:45
S32	1	S24 and (busy)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 11:21
S33	1	S24 and ("351")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 11:21
S34	1	10/036602	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:46
S35	1	S34 and snoop\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:46
S36	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:47

S37	0	S36 and multicast\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:47
S38	0	S36 and (multicast\$3 multi-cast\$3 broadcast\$3 broad-cast\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 13:15
S39	149	backpressure with threshold	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 13:27
S40	27	backpressure same threshold same overflow	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 13:35
S41	27	S40 and (packet cell)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 13:27
S42	7	(leaky leaky-bucket) same threshold same overflow	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 15:10
S43	106	(rts cts) same (multicast\$3 multi-cast\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 15:10
S44	51	(rts cts) same (multicast\$3 multi-cast\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 15:18
S45	1	"5875186".pn. and (multicast\$3 with rts)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 15:19
S46	1	"20030043835".pn.	US-PGPUB; USPAT	OR	ON	2005/10/25 08:51
S47	1	S46 and (enable\$3 same multi-sink)	US-PGPUB; USPAT	OR	ON	2005/10/25 08:54
S48	1	S46 and (enable\$3 )	US-PGPUB; USPAT	OR	ON	2005/10/25 09:13
S49	1	S46 and (snoop\$3)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:14
S50	240	(snoop\$3) with (accept\$3 reject\$3)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:16
S51	96	S50 and (packet cell)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:15

S52	108	(snoop\$3) with (accept\$3 reject\$3) with (packet cell data)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:16
S53	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 09:42
S54	1	S53 and ("351" same ("370" "390"))	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:53
S55	387381	"1" and (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:54
S56	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:54
S57	1	S56 and (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:55
S58	1	S56 and (bus same "302")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:55

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L1	14	("20030126233" "20010038633" "4769810" "6144636" "6212165" "6327246" "6460088" "6470016" "6480911" "6614758" "6625157" "6697362" "6700899" "6748435"). pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 14:09
L2	7	1 and (ring loop)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 14:10
L3	3	1 and (snoop\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 14:10